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UNITED STATES PATENT APPLICATION

OF

YONG SUNG HAM

FOR

IN-PLANE SWITCHING MODE LIQUID CRYSTAL DISPLAY DEVICE

This application claims the benefit of Korean Patent Application No. 1997-38863, filed on August 14, 1997, which is hereby incorporated by reference.

BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates to a liquid crystal display device, and more particularly, an in-plane switching mode liquid crystal display device.

Discussion of the Related Art

Twisted nematic liquid crystal display devices (hereinafter TN LCDs) having high image quality and low power consumption are widely applied to flat panel display devices. TN LCDs, however, have a narrow viewing angle due to refractive anisotropy of liquid crystal molecules. This is because horizontally aligned liquid crystal molecules prior to applying a voltage become nearly vertically aligned with respect to the substrate when voltage is applied to the liquid crystal panel.

To solve this problem, a multi-domain LCD such as a two-domain TN LCD(TDTN LCD) and a domain divided TN LCD(DDTN LCD), and a TN LCD including an optical compensation film have been introduced. In these LCDs, however, a contrast ratio is decreased and a color shift is generated according to a viewing angle.

Further, for the purpose of obtaining a wide viewing angle, an in-plane switching mode LCD has also been proposed.

FIG. 1A is a plan view of a unit pixel of a conventional in-plane switching mode active matrix LCD. As shown in the drawing, the apparatus comprises a data bus line 1 and a gate bus line 2 which are arranged perpendicularly and/or horizontally in a matrix on a transparent substrate thereby defining a unit pixel region. A common line 5 is arranged parallel to the gate bus line 2 in the pixel region. A thin film transistor (TFT) is formed adjacent a cross point of the data bus line 1 and the gate bus line 2. A common electrode 11 and a data electrode 19 are formed in the pixel region.

FIG. 1B is a sectional view according to line I-I of FIG. 1A. As shown in the drawing, the TFT includes a gate electrode 10 electrically coupled with the gate bus line 2, a gate insulator 13 on the gate electrode 10, an a-Si layer 15 on the gate insulator 13, an n+ a-Si layer 16 on the a-Si layer 15, and source/drain electrodes 17, 18 which are electrically coupled the data bus line 1 and the data electrode 19, respectively. The gate insulator 13 is formed on the inner surface of a first substrate 4. The common electrode 11 is formed with the gate electrode 10 and electrically coupled to the common line 5. Further, a passivation layer 20 and a first alignment layer(not illustrated) are deposited on the inner surface of the first substrate 4.

On a second substrate 6, a black matrix 7 is formed to prevent a light leakage around the TFT, the data bus line 1, and the gate bus line 2. A color filter layer 8, an over-coat layer 9, and a second alignment layer(not illustrated) are formed on the

black matrix 7 in sequence. Finally, a liquid crystal layer is formed between the first and second alignment layers.

The above conventional in-plane switching mode LCD, however, has a complex pattern in the pixel region thereby decreasing aperture ratio. The following formula (1) represents a light transmittance(T/T_0) in the liquid crystal layer of the conventional in-plane switching mode LCD.

$$T/T_0 = \sin^2(2\alpha) \sin^2(\pi d \Delta n / \lambda) \text{ - - - - - (1)}$$

,where α is an average rotating angle of liquid crystal directors, $d\Delta n$ is a retardation by the liquid crystal layer, d is a thickness of the liquid crystal layer, Δn is the refractive anisotropy of the liquid crystal layer, and λ is a wavelength of light.

As shown in the above formula, if each of liquid crystal molecules having fixed conditions α , and Δn is used, a desirable light transmittance can be achieved in accordance with the thickness d of the liquid crystal layer.

The Japanese Patent Unexamined Publication No.6-160878 proposed a TFT-LCD having a high light transmittance by providing a high aperture ratio and preventing a degradation failure in the pixel region. Further, the LCD may have been manufactured rapidly by choosing freely a liquid crystal compound and an alignment material. Furthermore, in this LCD, the increased light transmittance may have been achieved by the rotation of the liquid crystal molecules and by permitting $d\Delta n$ to be in a range of 0.21-0.36 μm .

In general, since a color-display in the LCD is based on the transmittance variation of the wavelength according to $d\Delta n$, it is essential to determine a desire $d\Delta n$ so as to display all colors.

However, JP No. 6-160878 only discloses $d\Delta n$ considering the light transmittance in the liquid crystal layer. The conventional TFT-LCD including JP No. 6-160878 does not disclose a desirable $d\Delta n$ that considers the affect of color-shift.

SUMMARY OF THE INVENTION

Accordingly, the present invention is directed to an in-plane switching mode LCD that substantially obviates one or more of the problems due to limitations and disadvantages of the related art.

An object of the present invention is to provide an in-plane switching mode LCD having a high light transmittance by controlling the refractive anisotropy of the liquid crystal molecule and the thickness of the liquid crystal layer.

Another object of the present invention is to provide an LCD that prevents color-shift.

Additional features and advantages of the invention will be set forth in the description which follows, and in part will be apparent from the description, or may be learned by practice of the invention. The objectives and other advantages of the invention will be realized and attained by the structure particularly pointed out in the written description and claims hereof as well as the appended drawings.

To achieve these and other advantages and in accordance with the purpose of the present invention, as embodied and broadly

described, an in-plane switching mode liquid crystal display device comprises first and second substrates; a plurality of gate and data bus lines defining pixel regions and arranged on the first substrate; a common line in the pixel regions; a pair of first and second electrodes parallel to each other applying plane electric fields in the pixel regions; and a liquid crystal layer between the first and second substrates; wherein $d\Delta n$ is in the range of 0.29-0.36 μm , where d is the thickness of the liquid crystal layer, and Δn is the refractive anisotropy of the liquid crystal molecule.

In another aspect of the present invention, a method of making an in-plane switching mode liquid crystal display device having first and second substrates comprises the steps of forming a plurality of gate and data bus lines defining pixel regions and arranged on the first substrate; forming a common line in the pixel regions; forming a pair of first and second electrodes parallel to each other applying plane electric fields in the pixel regions; and forming a liquid crystal layer between the first and second substrates; wherein $d\Delta n$ is in the range of 0.29-0.36 μm , where d is the thickness of the liquid crystal layer, and Δn is the refractive anisotropy of the liquid crystal molecule.

It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWING

The accompanying drawings, which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention and together with the description serve to explain the principles of the invention.

In the drawings:

FIG. 1A is a plan view of a unit pixel of a conventional in-plane switching mode LCD;

FIG. 1B is a sectional view according to line I-I of FIG. 1A;

FIG. 2 is a graph showing a variation of light transmittance according to a variation of retardation; and

FIG. 3 is a chromaticity diagram showing a white color-shift in color triangle.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Reference will now be made in detail to the preferred embodiment of the present invention, example of which is illustrated in the accompanying drawings.

Although not shown in the drawing, an LCD according to the present invention comprises a first substrate, a second substrate and a liquid crystal layer between the first and second substrates. On the first substrate, a data bus line and a gate bus line arranged perpendicularly and/or horizontally in a matrix on a transparent substrate thereby defining a unit pixel region. A common line is arranged parallel to the gate bus line in the pixel region. A thin film transistor is formed adjacent a cross point of the data bus line and the gate bus line. A common

CelCN(cellulosecinamate).

Color-shift which is generated in an in-plane switching mode LCD is influenced by a back light, a color filter, and $d\Delta n$ of liquid crystal layer. In particular, conventional back light and color may be used, but $d\Delta n$ is chosen to prevent color-shift. Moreover, the $d\Delta n$ is chosen to maintain white color balance.

FIG. 2 is a graph showing a variation of light transmittance according to a variation of retardation. As shown in the graph, $S+S'$ region is a limited region according to the prior art indicating that $d\Delta n$ is in the range of $0.21-0.36\mu m$, and S' region is a further limited region according to the present invention.

FIG. 3 is a chromaticity diagram showing a white color-shift in color triangle. A plurality of points in Fig. 3 representing $d\Delta n$ is in the range of $0.21-0.40\mu m$. A subset of these points are in a dotted line rectangle (white color display region) 30 and is in the range of $0.29-0.36\mu m$ as shown in FIG.2 of the present invention.

If $d\Delta n$ is in the range of $0.21-0.29\mu m$, it is impossible to display the preferable white color. However, in the present invention, $d\Delta n$ is in the range of $0.29-0.36\mu m$ and all points are in the inner region of the dotted line rectangle.

In accordance with the present invention, it is possible to increase the light transmittance by controlling the refractive anisotropy of the liquid crystal molecule and the thickness of the liquid crystal layer.

Further, it is possible to prevent color-shift in the LCD of

electrode and a data electrode are in the pixel region.

The TFT includes a gate electrode electrically coupled with the gate bus line, a gate insulator made of a material such as SiNx and SiOx on the gate electrode, an amorphous silicon layer on the gate insulator, a n⁺ amorphous silicon layer on the a-Si layer, and source/drain electrodes which are electrically coupled the data bus line and the data electrode respectively.

The common electrode is formed with the gate electrode and electrically coupled to the common line. Also, a passivation layer made of a material such as SiNx and SiOx, and a first alignment layer are deposited on the inner surface of the first substrate.

On the second substrate, a black matrix such as CrOx and black resin is formed to prevent a light leakage around the TFT, the data bus line, and the gate bus line. A color filter layer including color filter elements R, G, and B, an over-coat layer, and a second alignment layer are formed on the black matrix in sequence.

The liquid crystal layer is formed between the first and second alignment layers. The thickness of the liquid crystal is chosen considering the light transmittance and the color-shift.

Each alignment direction of the first and second alignment layers is determined by a rubbing method using polyamide, polyimide, SiO₂, PVA(polyvinylalcohol) or polyamic acid, or by photo-alignment method using a photosensitive material such as PVCN (polyvinylcinnamate), PSCN (polysiloxanecinnamate) or

the present invention, especially white color-shift.

Accordingly, the in-plane switching mode liquid crystal display device of the present invention comprises elements of an LCD such as transparent substrates, electrodes, and alignment layer and so on. In the LCD device of the present invention, thickness of the liquid crystal layer is determined by considering the color-shift as well as the light transmittance. Preferably, $d\Delta n$ of the present invention is in a range of 0.29-0.36 μm , thereby preventing the LCD from color-shift, especially white color-shift.

It will be apparent to those skilled in the art that various modifications and variation can be made in the in-plane switching mode liquid crystal display of the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.

WHAT IS CLAIMED IS:

1. An in-plane switching mode liquid crystal display device comprising:

5 first and second substrates;
/ a plurality of gate and data bus lines defining pixel regions and arranged on the first substrate;
a common line in the pixel regions;
a pair of first and second electrodes parallel to each other
10 applying plane electric fields in the pixel regions; and
a liquid crystal layer between the first and second substrates;

wherein $d\Delta n$ is in the range of 0.29-0.36 μm , where d is the thickness of the liquid crystal layer, and Δn is the refractive anisotropy of the liquid crystal molecule.

2. The in-plane switching mode liquid crystal display device according to claim 1, wherein the first electrode includes data electrode and the second electrode includes common electrode.

3. The in-plane switching mode liquid crystal display device according to claim 1, further comprising:

a plurality of thin film transistors adjacent
respective cross points of the gate and data bus lines, each of
25 the thin film transistors including a gate electrode, a gate

insulator, a semiconductor layer, and source and drain electrodes;
a passivation layer on the thin film transistors; and
a first alignment layer on the passivation layer.

5 4. The in-plane switching mode liquid crystal display device
according to claim 3, wherein the passivation layer includes one
of SiNx and SiOx.

10 5. The in-plane switching mode liquid crystal display device
according to claim 3, wherein the first alignment layer comprises
one of polyamide, polyimide, SiO₂, polyvinylalcohol and polyamic
acid.

15 6. The in-plane switching mode liquid crystal display device
according to claim 3, wherein the first alignment layer comprises
photosensitive materials.

20 7. The in-plane switching mode liquid crystal display device
according to claim 6, wherein the photosensitive material is
selected from the group consisting of polyvinylcinnamate,
polysiloxanecinnamate and cellulosecinnamate.

8. The in-plane switching mode liquid crystal display device
according to claim 3, further comprising:

25 a black matrix for preventing light from leaking around the
TFTs, gate bus line, and data bus line;

a color filter layer on the second substrate; and
a second alignment layer on the color filter layer.

9. The in-plane switching mode liquid crystal display device
5 according to claim 8, wherein the second alignment layer comprises
/ one of polyamide, polyimide, SiO_2 , polyvinylalcohol and polyamic
acid.

10. The in-plane switching mode liquid crystal display device
10 according to claim 8, wherein the second alignment layer comprises
a photosensitive material.

11. The in-plane switching mode liquid crystal display device
according to claim 10, wherein the photosensitive material is
15 selected from the group consisting of polyvinylcinnamate,
polysiloxanecinnamate and cellulosecinnamate.

12. A method of making an in-plane switching mode liquid crystal
display device having first and second substrates, the method
20 comprising the steps of:

/ forming a plurality of gate and data bus lines defining pixel
regions and arranged on the first substrate;

forming a common line in the pixel regions;

forming a pair of first and second electrodes parallel to
25 each other applying plane electric fields in the pixel regions;
and

forming a liquid crystal layer between the first and second substrates;

wherein $d\Delta n$ is in the range of 0.29-0.36 μm , where d is the thickness of the liquid crystal layer, and Δn is the refractive anisotropy of the liquid crystal molecule.

13. The method according to claim 12, wherein the first electrode includes data electrode and the second electrode includes common electrode.

14. The method according to claim 12, further comprising the steps of:

forming a plurality of thin film transistors adjacent respective cross points of the gate and data bus lines, each of the thin film transistors including a gate electrode, a gate insulator, a semiconductor layer, and source and drain electrodes; forming a passivation layer on the thin film transistors; and forming a first alignment layer on the passivation layer.

15. The method according to claim 14, wherein the passivation layer includes one of SiNx and SiOx .

16. The method according to claim 14, wherein the first alignment layer comprises one of polyamide, polyimide, SiO_2 , polyvinylalcohol and polyamic acid.

17. The method according to claim 14, wherein the first alignment layer comprises photosensitive materials.

18. The method according to claim 17, wherein the photosensitive material is selected from the group consisting of polyvinylcinnamate, polysiloxanecinnamate and cellulosecinnamate.

19. The method according to claim 14, further comprising the steps of:

forming a black matrix for preventing light from leaking around the thin film transistors, gate bus line, and data bus line;

forming a color filter layer on the second substrate; and forming a second alignment layer on the color filter layer.

20. The method according to claim 19, wherein the second alignment layer comprises one of polyamide, polyimide, SiO_2 , polyvinylalcohol and polyamic acid.

21. The method according to claim 19, wherein the second alignment layer comprises a photosensitive material.

22. The method according to claim 21, wherein the photosensitive material is selected from the group consisting of polyvinylcinnamate, polysiloxanecinnamate and cellulosecinnamate.

ABSTRACT OF THE DISCLOSURE

An in-plane switching mode liquid crystal display device includes first and second substrates, a plurality of gate and data bus lines defining pixel regions and arranged on the first substrate, a common line in the pixel regions, a pair of first and second electrodes parallel to each other applying plane electric fields in the pixel regions, and a liquid crystal layer between the first and second substrates, $d\Delta n$ being in the range of 0.29-0.36 μm , where d is the thickness of the liquid crystal layer, and Δn is the refractive anisotropy of the liquid crystal molecule.

FIG. 1A

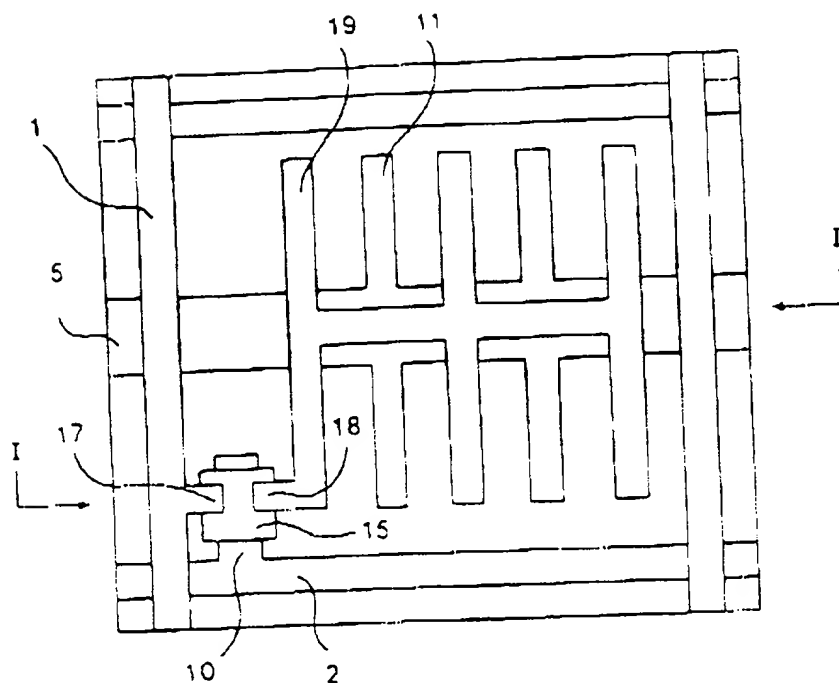


FIG. 1B

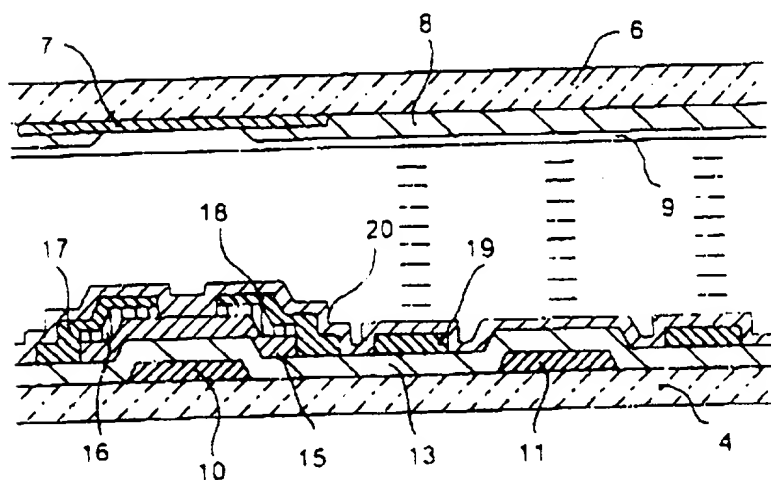


FIG.2

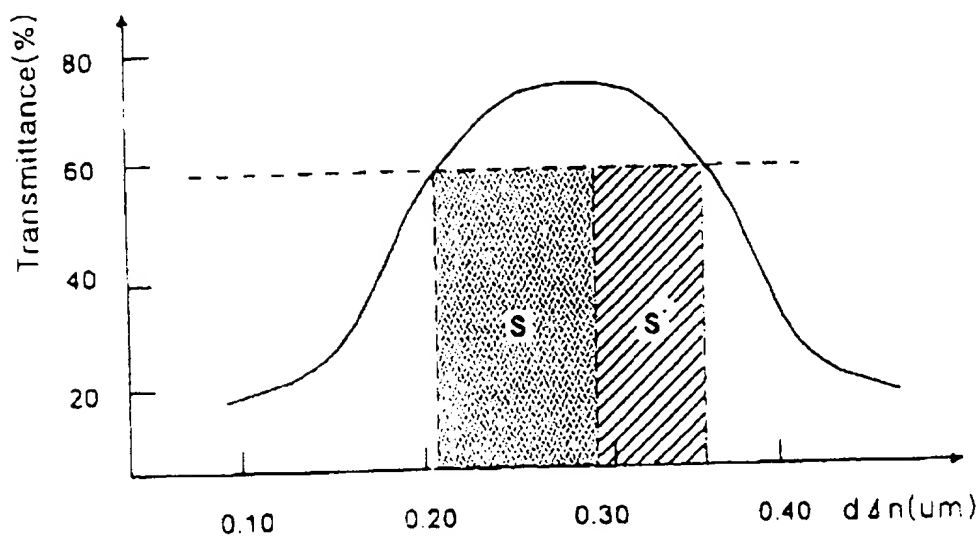
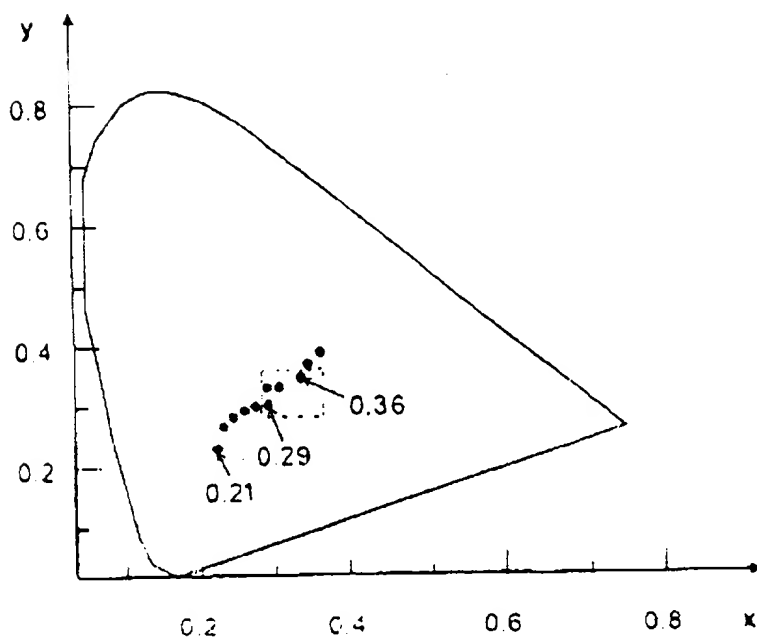
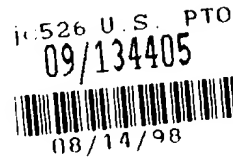
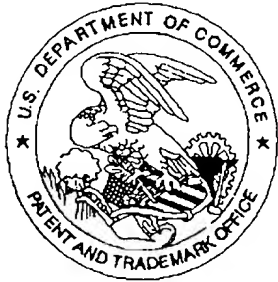


FIG.3



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